

SIMULTANEOUS SWITCHING OUTPUT BOUNCE REDUCTION DEVICE AND METHOD

ABSTRACT OF THE DISCLOSURE

A signal bounce inhibiting device and a signal bounce inhibiting method are provided for preventing power/ground bounce resulting from simultaneous switching output. The signal bounce inhibiting device includes an electric level toggling circuit and an electric level recovering circuit. The electric level toggling circuit receiving an internal signal of the first chip, and toggling the internal signal into a first output signal in response to a first toggling control signal. The electric level recovering circuit receiving the first output signal, and recovering the first output signal into the internal signal required by a second chip in response to a first recovering control signal.